

DETAILED ACTION

1. This action is in response to the amendment filed 3/26/2008.
2. Claims 1, 4, and 10-33 are allowed.

Examiner's Amendment

3. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Examiner in his previous action indicated that claim 1 is allowable if the features of claims 2, 5, 6, and 9 are incorporated into it. However, after further reviewed and searched, the examiner believes that claims 7 and 8 are also needed to incorporate into claim 1 for further clarify the cacheable condition. A telephone interview with Mr. Robert Lord on 7/1/2008 was made to seek an examiner's amendment. Authorization for this examiner's amendment was given in a telephone interview with Mr. Robert Lord (Reg. No 46,479) on 7/17/2008. Examiner's amendment is necessitated to further clarify the claimed invention.

Per applicants' request, claims 1 and 28 have been further amended below; claims 7 and 8 have been canceled.

1. (Currently Amended) A method for caching in a tracing framework, comprising:
firing a probe associated with a thread;
evaluating a first predicate of the probe to determine a first Boolean value,
wherein the first Boolean value is false;
determining that the first predicate is cacheable, wherein the first predicate is
cacheable when it is the first predicate is referencing on selected from a group
consisting of an immutable variable and a thread-specific variable;
identifying the first predicate using a predicate cache identifier;
storing the, predicate cache identifier with the probe as a probe cache identifier;
caching the first Boolean value and the predicate cache identifier in a predicate
cache associated with the thread, based on the first Boolean value being false and the
first predicate being cacheable the evaluating of the first predicate and cacheability of
the first predicate; and
transferring control to the thread, based on the caching.

7. (Cancelled)

8. (Cancelled)

28. (Currently Amended) A computer system for caching in a tracing framework
comprising:
a processor;

a memory;

a storage device; and

software instructions stored in memory for enabling the computer system to:

- fire a probe associated with a thread;
- evaluate a predicate of the probe to determine a Boolean value, wherein the Boolean value is false;
- determine that the predicate is cacheable, wherein the predicate is cacheable when is the predicate is predicate is referencing one selected from a group consisting of an immutable variable and a thread-specific variable;
- identify the predicate using a predicate cache identifier;
- store the predicate cache identifier with the probe as a probe cache identifier;
- cache Boolean value and the predicate cache identifier in a predicate cache associated with the thread, based on tile Boolean value being false and the predicate being cacheable; and
- transfer control to the thread, based on the caching.

Examiner's Statement of Reason(s) for Allowance

4. The following is an examiner's statement of reasons for allowance:

The prior art of record i.e. Bryant (USPN 6,728,949), taken alone or in combination with other prior art fails to teach or reasonably suggest *storing the predicate cache identifier with the probe as a probe cache identifier; caching the first*

Boolean value and the predicate cache identifier in a predicate cache associated with the thread, based on the first Boolean value being false and the first predicate being cacheable as recited in claim 1. Similar concepts are found in independent claim 28.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phillip H. Nguyen whose telephone number is (571) 270-1070. The examiner can normally be reached on Monday - Thursday 10:00 AM - 3:00 PM EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wei Y. Zhen can be reached on (571) 272-3708. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2191

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

7/18/2008

PN

/Wei Zhen/

Supervisory Patent Examiner, Art Unit 2191